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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/790,590	03/01/2004	Todd P. Lukanc	H1775	9600
61060 WINSTEAD PO	7590 04/07/200 C	9	EXAM	IINER
P.O. BOX 5078		WHITMORE, STACY		
DALLAS, TX 7	7,3201		ART UNIT	PAPER NUMBER
			2825	
			MAIL DATE	DELIVERY MODE
			04/07/2009	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Supplemental					
Notice	of Allowability				

Application No.	Applicant(s)	
10/790,590	LUKANC ET AL.	
Examiner	Art Unit	
Stacy A. Whitmore	2825	

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The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.							
1. 🔀 This communication is responsive to <u>Decision from BPAI d</u>	ated 11/20/2008 .						
2. ☑ The allowed claim(s) is/are <u>11 and 19, renumbered as 1-2</u> .							
Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a)							
Attachment(s) 1. ☐ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☑ Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date 7/1/2004 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	5. ☐ Notice of Informal F 6. ☐ Interview Summary Paper No./Mail Da 7. ☑ Examiner's Amend 8. ☐ Examiner's Statement	(PTO-413), te ment/Comment	owance				
/Stacy A Whitmore/ Primary Examiner, Art Unit 2825							

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Notice of Allowance

1. Claims 11 and 19 are allowed.

- 2. The Board of Patent Appeals and Interferences affirmed the rejection(s) against independent claim(s) 1 and 31, but reversed all rejections against claim(s) 11 and 19 dependent thereon. There are no allowed claims in the application. The independent claim(s) is/are cancelled by the examiner in accordance with MPEP § 1214.06.
- 3. Claim 11 is rewritten in independent form, and claim 19 is rewritten in dependent form, as it is dependent form claim 11.
- 4. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

The application has been amended as follows:

- I. Cancel claims 1-4, 6-9, 12-18, 20, and 31.
- II. In claim 11, delete claim 11 and insert in it's place --

A method of designing an integrated circuit (IC) device having desired electrical characteristics, said method comprising:

providing an initial IC device design;

generating a layout representation corresponding to the initial IC device design; simulating how structures within the layout representation will pattern on a wafer; based on the simulating step, determining whether actual electrical characteristics associated with the initial IC device design sufficiently match the desired electrical characteristics,

wherein the desired electrical characteristics include at least one of gain and switching speed; and if the actual electrical characteristics associated with the initial IC device design do not sufficiently match the desired electrical characteristics, modifying the initial IC device design;

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wherein the initial IC device design includes a desired relationship between at least two structures within the IC device design;

further comprising:

determining an amount of process-related variation associated with at least two structures within the layout representation of the IC device design;

wherein determining an amount of process-related variation associated with at least two structures within the layout representation includes:

simulating how structures within the layout representation will pattern on a wafer; and measuring a feature of the simulated structures, said feature being indicative of process-related variation;

wherein the feature indicative of process-related variation is at least one of (i) slope of edge intensity and (ii) logarithm of slope of edge intensity; and wherein: a larger slope of edge intensity or logarithm of slope of edge intensity is indicative of a smaller process-related variation; and a smaller slope of edge intensity or logarithm of slope of edge intensity is indicative of a larger process-related variation. --.

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Stacy A. Whitmore whose telephone number is (571) 272-1685. The examiner can normally be reached on Monday-Thursday, alternate Friday 6:30am - 4:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jack Chiang can be reached on (571) 272-7483. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Stacy A Whitmore/ Primary Examiner Art Unit 2825

SAW

March 4, 2009